



## TRAINING

- Expert Training in the Latest Technologies
- Industry-Demanded Certifications

## PCB TECHNOLOGY

### Quality & Inspection

- IPC-A-610 Instructor & Operator Certification

### Soldering & Assembly

- IPC J-STD-001 Instructor & Operator Certification

### Bare Board Inspection

- IPC-A-600 Instructor & Operator Certification
- IPC-6012 Instructor & Operator Certification

### Rework & Repair

- IPC-7711 & IPC-7721 Instructor & Operator Certification

### Hand Soldering Skills

- Soldering Basics, Wires & Terminals, Lap Solder Joints, Through-Hole and Surface Mount Training

### PCB Fundamentals

- Component Identification
- Electrostatic Discharge

### PCB Design

- Essentials of PCB Design
- IPC Designer Certification

## COUNTERFEIT COMPONENTS

### IDEA-STD-1010

- Seminars & Workshops
- IDEA-STD-1010 Essentials
- SAE AS5553 Counterfeit Electronics

## CABLE & WIRE

### HARNESST TECHNOLOGY

### Quality & Inspection

- IPC-A-620 Instructor & Operator Certification

### Hands-On Labs

- Crimping & Harness Assembly Training

## TECHNICAL SUPPORT

- Manufacturing Start-Up
- Process Evaluation
- Subcontractor Qualification
- Equipment Evaluation
- Lead-Free, ESD, Process and Quality Audits

## J-STD-001 SPACE ELECTRONICS HARDWARE ADDENDUM

Optional Module to IPC's J-STD-001 Instructor, Operator and Standard Expert Training & Certification Program

### J-STD-001

**Optional, half-day course. May be attended in conjunction with the J-STD-001 Certified IPC Trainer (CIT), Certified IPC Specialist (CIS) or Certified Standards Expert (CSE). Students must bring their own copy of the J-STD-001.**

### COURSE DESCRIPTION

The J-STD-001 Space Electronics Hardware Addendum provides additional requirements over those published in IPC J-STD-001 to ensure the reliability of soldered electrical and electronic assemblies that must survive the vibration and thermal cyclic environments experienced while traveling and operating in space.

This half-day program consists of a lecture which will identify and review each section of the addendum, followed by a single open book exam.

### WHO SHOULD BECOME CERTIFIED

Suppliers to NASA and others who are mandated to use J-STD-001 Space Electronics Hardware Addendum are required to have workers trained and certified to this IPC J-STD-001 course for Specialist (CIS, Instructor (CIT) and Expert (CSE) level training. This includes trainers, quality supervisors, engineers and manufacturing supervisors with assembly responsibilities.

### PREREQUISITES

All attendees must be certified to the latest J-STD-001 revision. CIS attendees must be certified in Module 1 and at least one other module before attending the J-STD-001 Space Electronics Hardware Addendum Program.

### CLASS SIZE

Maximum number of students is limited to ten (10) to provide greater instructor interaction. Call early to reserve your space.

**eTRAINING** On-line training is available for some courses. Please inquire.

**ON-SITE TRAINING** Please call a training consultant and ask about customized course content, on-site training and training around your production schedules.

**REGISTRATION** For up to date pricing and more information on any of the EPTAC programs, or to enroll, please call us toll free or visit eptac.com.

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**Web:** eptac.com